# Printed Circuits Board (PCB) Design

ELC 4438: Embedded Systems Design Spring 2016

## What is a PCB? A Mechanical Solution to an Electrical Problem





Photo from hephaestusaudio.com

Photo from www.sparkfun.com

# What is a PCB? Layers, Traces, Planes, and Vias ...





Image from <a>www.elkosoft.com</a>

# What is a PCB? Essentially, your job is this ...



Image from <a>www.gettyimages.com</a>

# The PCB Design Process From Vision To Reality



# PCB Design Software A CAD Tool For Designing a Board



#### PCB Design Software Symbol + Footprint + Device = Component

![](_page_6_Figure_1.jpeg)

# PCB Design Stuff I Need To Get Started

#### & A Plan

ø Block Diagram

- ø Component Selection
- ø Connections and Testing Considerations
- *φ* Power and Performance Considerations
- & A New Project
- & Access to Libraries

# PCB Design It's all about the planning.

Components

#### Connections

#### Power & Performance

Passives: 0805, 0603, etc.

Mechanical Connections

Power Ratings

IC Packages: QFN, TQFP, etc.

**Bus Connections** 

Battery Performance

Libraries

PC Connections

High-Speed / High-Sensitivity

# Schematic Capture Creating a New Schematic

# File -> New -> Schematic

• Keep a grid.

![](_page_9_Picture_4.jpeg)

# Schematic Capture

#### Adding Components

- Click the Add icon
- Find the component in the library
- Set values
- Don't see the library? Try Use -> Library

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# Schematic Capture Adding Global Symbols

- Click the Add icon
- Find the global symbol in the library (supply1)
- Place as if it were a component

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# Schematic Capture Adding Traces (aka Wires)

#### • Click the Wire icon

![](_page_12_Figure_3.jpeg)

# Some General Tips

- Avoid changing the grid unless absolutely necessary.
- Use multiple "sheets".
- Schematic drives the layout ...
- Groups, Info, and Layers icons are useful.
- Pan, zoom easy to use.
- Really care about that "net"? Label it.
- Keep it clean don't be lazy.
- Document!

#### Place Components But before you do ...

- Passives 0805 means 0.08" by 0.05"
- Connectors beware of physical fit
- Actives lots of packaging alternatives
- Give thought to testability and troubleshooting

![](_page_14_Figure_6.jpeg)

## Place Components Creating a new board design

- From the schematic, choose File ->
  Switch to Board.
- "Create From Schematic"
- Board window automatically opens.

![](_page_15_Figure_5.jpeg)

## Place Components Some Common Tasks

- Right-click on top of component, select
  Move to reposition.
- While moving, rightclick to Rotate.
- Use "Group" wisely.
- Right-click to Mirror (change to other side of board)
- Use Move to change outline

![](_page_16_Figure_7.jpeg)

### Place Components Thinking ahead

- Take your time.
- Untangle flight lines.
- Think about planes.
- Consider testability.
- Mechanical Fit?
- Hard stuff first.
- Keep silkscreen.
- Think hard about components on 2-sides.

![](_page_17_Figure_10.jpeg)

Place Components

#### Place Components Some General Tips

THE	Layers	Clearance	Distance	Sizes	Restring	Shapes	Supply	MIdSKS	MISC	
Different Signals										
				Wire						
			Wire 8	nil		Pad				
			Pad 8	nil	8r	nil		Via		
			Via 8	mil	8r	nil	8	8mil		
	-	-				Same Sigr	nals			
			_	Smd		Pad		Via		
			Smd 8	nil	8r	nil	8	mil		
Minim	um Clear	ance between	obiects in	signal lav	ers.					
The Sa	me Signa	Is check betw	een <i>Smd</i> ar	d Via doe	s not apply t	o Micro Vi	25.			
Setting	the value	s for the Sam	e Signals (	hecks to (	) disables th	e respectiv	e check			
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- Are your Design Rules setup correctly?
- Run the Design Rule Check (DRC) often.
- Run the Electrical Rule Check (ERC) often.
- Save a snapshot of your placement file.

Route Traces Adding "Waaaahrs" To Your Layout Schematic Capture Place Components Route Traces

- You want to *route*, not wire.
- You want to *ripup*, not *delete* nets.
- Are you on the top or the bottom?
- Vias are on top *and* bottom. Easy to add.
- DRC often!
- Iterate like mad.

![](_page_19_Picture_8.jpeg)

Schematic Capture Place Components Route Traces

### Route Traces Some General Tips

- Two layers? Have an xlayer and a y-layer.
- Don't forget mounting holes, other nice things.
- Add testability features.

![](_page_20_Figure_5.jpeg)

#### Generating Gerbers Some General Tips

3 CAM Processor - /Applications/EAGLE/cam/AdvancedCircuitsALL.cam - EAGLE 5.6.0 Light 000 Paste Drill Component side Solder side Silk screen CMP Solder stop mask CMP Solder stop mask SOL Nr 🔺 Layer Style Job 1 Top Mirror 16 Bottom Section Solder stop mask SOL 17 Pads Rotate Prompt 18 Vias Upside down 19 Unrouted ✓ pos. Coord 20 Dimension Output Quickplot 21 tPlace + ✓ Optimize 22 bPlace Device GERBER RS274X 23 tOrigins ▼ Fill pads 24 bOrigins 25 tNames 26 bNames File /gerber/%N.sts 27 tValues 28 bValues 29 tStop Offset 30 bStop 31 tCream X 0inch 32 bCream 33 tFinish Y 0inch 34 bFinish 35 tGlue 36 bGlue Process Job Process Section Description Add Del /Users/bob/Documents/eagle/KST\_RPS/kst\_rps\_revB.brd

Using Advanced Circuits? Have I got a script for you ...

Generate critical layers.

Place Components

Schematic Capture

Route Traces

Gen Gerbers

### Generating Gerbers Sanity Checking The Result

![](_page_22_Picture_1.jpeg)

Schematic Capture

**Place Components** 

Route Traces

Gen Gerbers

#### freedfm.com

- You'll need an account with Advanced Circuits.
- Email will be sent confirming design rule checking.
- Advanced Circuits even gives you a discount.
- Squash any errors, review all warnings. Look for open vias, closed vias, etc.

#### Generating Gerbers Reviewing The Result

Schematic Capture Place Components Route Traces Gen Gerbers

![](_page_23_Figure_2.jpeg)

- ViewMate Free Gerber Viewer for Microsoft Windows
- Review planes, drill holes, and "obvious" defects. Not usually any surprises here.
- Biggest error not exporting the proper layers.

#### Fab Before you hit "the button" ...

Schematic Capture Place Components Route Traces Gen Gerbers Fabl

- Generate a Parts List (aka "The BOM") using Export -> Partlist.
- Generate a Pin List (aka "The Netlist") using Export -> Netlist.
- Sanity check both.

000	0			BOM								
Partlist												
Exported from ledTest.sch at 10/16/09 7:23 AM												
EAGLE Version 5.6.0 Copyright (c) 1988–2009 CadSoft												
Part	Value	D	evice	Package	Library	Sheet						
BAT1		В	ATTERYAAA	BATTERY-AAA	SparkFun	1						
LED1		L	ED1206	LED-1206	SparkFun	1						
R1	330	R	ESISTOR0603-R	ES 0603-RES	SparkFun	1						
O O O D pinlist												
Pinlist												
Exported from ledTest.sch at 10/16/09 7:28 AM												
EAGLE Version 5.6.0 Copyright (c) 1988-2009 CadSoft												
Part	Pad	Pin	Dir	Net								
BAT1	GND@1	-	Pwr	GND								
	PWR@1	+	Pwr	N\$1								
LED1	A	A	Pas	N\$1								
	С	С	Pas	N\$2								
R1	1	1	Pas	N\$2								
	2	2	Pas	GND								

#### Fab Some General Tips

Schematic Capture Place Components Route Traces Gen Gerbers Fabl

- Do you have all of your components? Lead time?
- Find out what file format your PCB Vendor wants.
- Advanced Circuits GERBER\_RS\_274X
- Consider routing it yourself.
- Consider barebonespcb.com. Really cheap.
- Sleep on it.

#### The PCB Design Process A Summary of a Lot of Information

![](_page_26_Figure_1.jpeg)