

ELC 4351: Digital Signal Processing

Liang Dong

Quantizatio Errors

Signal Quantization

Signal to Quantization Noise Ratio

Coefficient Quantization

Roundoff Noise

Overflow

Scaling or Signals

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Quantization Errors

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Scaling o Signals Errors in Computing Systems:

 Numbers are represented by a finite number of bits. The resulting errors are called the finite-wordlength or finite-precision effects.



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Quantization errors: Signal quantization Coefficient quantization



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Quantization errors: Signal quantization Coefficient quantization

 Arithmetic errors: Roundoff or truncation Overflow



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Scaling or Signals • Analog signal $x(t) \Rightarrow ADC \Rightarrow digital signal x[n]$.



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- Analog signal $x(t) \Rightarrow ADC \Rightarrow digital signal x[n]$.
- First, x(t) is sampled and becomes a discrete-time signal x(nT).



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- Analog signal $x(t) \Rightarrow ADC \Rightarrow digital signal x[n]$.
- First, x(t) is sampled and becomes a discrete-time signal x(nT).

Then, x(nT) is encoded using B bits and becomes a digital signal x[n].



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Dynamic range = 2.



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Scaling of Signals • Suppose that $-1 \le x[n] < 1$.

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• *B* bits represent a sample, the number of quantization levels is 2^{B} .



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Scaling of Signals • Suppose that $-1 \le x[n] < 1$.

Dynamic range = 2.

• *B* bits represent a sample, the number of quantization levels is 2^{B} .

• The quantization step (resolution): $\Delta = \frac{2}{2^B} = 2^{-B+1}$.



Rounding for Quantization

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Scaling of Signals

A 3-bit ADC:





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Scaling o Signals Quantization error/noise: e(n) = x(n) - x(nT).



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• Rounding: $|e(n)| \leq \Delta/2$.



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The quantization noise depends on the quantization step.

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- Quantization error/noise: e(n) = x(n) x(nT).
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• More bits \Rightarrow smaller quantization step \Rightarrow lower quantization noise.



Linear Model

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Scaling of Signals • The nonlinear operation of quantizer: x(n) = Q[x(nT)]

• Linear operation: x(n) = Q[x(nT)] = x(nT) + e(n)





Common Assumptions

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Scaling or Signals Assume that the quantization error e(n) is uncorrelated with x(n).



Common Assumptions

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Scaling of Signals Assume that the quantization error e(n) is uncorrelated with x(n).

 Assume e(n) is a random variable uniformly distributed in the interval [-Δ/2, Δ/2].

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Common Assumptions

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Scaling of Signals Assume that the quantization error e(n) is uncorrelated with x(n).

 Assume e(n) is a random variable uniformly distributed in the interval [-Δ/2, Δ/2].

• Therefore, $E[e(n)] = (-\Delta/2 + \Delta/2)/2 = 0;$

and variance:
$$\sigma_e^2 = \frac{\Delta^2}{12} = \frac{2^{-2B}}{3}$$

Large wordlength B leads to small quantization error σ_e^2 .



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Scaling of Signals • SNR = $10 \log_{10}(\sigma_x^2/\sigma_e^2)$.

• With $\sigma_e^2 = 2^{-2B}/3$, we have

$$SNR = 10 \log_{10}(3 \times 2^{2B} \sigma_x^2)$$

 $= 10 \log_{10} 3 + 20 B \log_{10} 2 + 10 \log_{10} \sigma_x^2$

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 $= 4.77 + 6.02B + 10\log_{10}\sigma_x^2$



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= $4.77 + 6.02B + 10 \log_{10} \sigma_x^2$

For each additional bit, the ADC provides about 6-dB gain.



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For each additional bit, the ADC provides about 6-dB gain.

SNR is proportional to σ_x^2 . Keep signal power as large as possible.



Coefficient Quantization

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Scaling of Signals • The filter coefficients b_n , a_m are quantized for a given fixed-point processor.



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- The filter coefficients b_n , a_m are quantized for a given fixed-point processor.
- Coefficient quantization can cause serious problems if the poles of designed IIR filters are too close to the unit circle.



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- The filter coefficients b_n , a_m are quantized for a given fixed-point processor.
- Coefficient quantization can cause serious problems if the poles of designed IIR filters are too close to the unit circle.
- This is because those poles may move outside the unit circle due to coefficient quantization, resulting in an unstable implementation.



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 $y(n) = \alpha x(n)$



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$$y(n) = \alpha x(n)$$

• x(n) and α are *B*-bit, the product y(n) will be 2*B*-bit.

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Usually, the result will be stored in *B*-bit memory.



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- Truncation or rounding brings the roundoff noise.



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$$y(n) = Q[\alpha x(n)] = \alpha x(n) + e(n)$$



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- Usually, the result will be stored in *B*-bit memory.
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$$y(n) = Q[\alpha x(n)] = \alpha x(n) + e(n)$$

Is this noise larger?



Overflow

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Scaling o Signals When the dynamic range of signals is fixed, the result of an arithmetic addition may exceed the capacity of the register.



Overflow

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Scaling or Signals

- When the dynamic range of signals is fixed, the result of an arithmetic addition may exceed the capacity of the register.
- This overflow results in severe distortion of the signal output.



Overflow

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• We need saturation algorithm or proper scaling.



Saturation Algorithm

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Scaling o Signals Saturation arithmetic prevents overflow by keeping the result at a maximum value.

 Saturation algorithm is a nonlinear operation that clips the desired waveform.





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Scaling of Signals • An effective technique in preventing overflow is by scaling down the signal.



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Scaling of Signals • An effective technique in preventing overflow is by scaling down the signal.



If the signal x(n) is scaled by β, the corresponding signal variance changes to β²σ²_x.



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• SNR = $10 \log_{10} (\beta^2 \sigma_x^2 / \sigma_e^2)$ = 4.77 + 6.02B + $10 \log_{10} \sigma_x^2 + 20 \log_{10} \beta$



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For down scaling, $\beta < 1$.



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For down scaling, $\beta < 1$.

• The term $20 \log_{10} \beta$ is negative, and the SNR reduces.



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- The term $20 \log_{10} \beta$ is negative, and the SNR reduces.
- For example, when $\beta = 0.5$, $20 \log_{10} \beta = -6.02$ dB, thus reducing the SNR of the input signal by about 6 dB.



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For down scaling, $\beta < 1$.

- The term $20 \log_{10} \beta$ is negative, and the SNR reduces.
- For example, when $\beta = 0.5$, $20 \log_{10} \beta = -6.02$ dB, thus reducing the SNR of the input signal by about 6 dB.
- This is equivalent to losing 1 bit in representing the signal. Why?